

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)	
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Hidetada TOKIOKA et al.)	Group Art Unit: Unassigned
)	
Application No.: Unassigned)	Examiner: Unassigned
)	
Filed: June 24, 2003)	Confirmation No.: Unassigned
)	
For: CURRENT SUPPLY CIRCUIT AND)	
DISPLAY APPARATUS)	
INCLUDING THE SAME)	

**FIRST
INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

The document is being submitted within three (3) months of the filing or entry of the national stage of this application or before the first Office Action on the merits, whichever is later. Since document is being filed within the time period set forth in 37 C.F.R. § 1.97(b) no fee or statement is required.

To assist the Examiner, the document is listed on the attached form PTO-1449. It is respectfully requested that an Examiner-initialed copy of this form be returned to the undersigned.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

Date: June 24, 2003

By: 

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APPLICATION No.
Unassigned

APPLICANT
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GROUP
Unassigned

[illegible][illegible][illegible]Date
Considered

***Also discussed in the specification.**